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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/052,989	11/09/2001	Han-Kun Hsieh	YUSO-131	1309

7590
Raymond Sun
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Tustin, CA 92782

EXAMINER

VU, DAVID

ART UNIT	PAPER NUMBER
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2818

DATE MAILED: 05/18/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/052,989

Applicant(s)

HSIEH ET AL.

Examiner

DAVID VU

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 24-43 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 24-43 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 November 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Continued Examination Under 37 CFR 1.914

1. A request for continued examination under 37 CFR 1.114, including the, fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on March 05, 2004 has been entered.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 34-43 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The specification and original claims do not include the limitation of "without

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performing any roughness processing to said contact pad” (as in claim 34), and thus this limitation is considered “new matter”.

Any Arguments regarding this “new matter” rejection should include the location in the original disclosure where the subject matter can be found.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 24-27, 32-37, 42 and 43 are rejected under 35 U. S. C. 102(e) as being anticipated by Higdon et al. (US Pat. 6,375,062, herein after Higdon).

Regarding claim 24, Higdon discloses a method of forming electroplated solder on an organic circuit board for making flip chip joints and board to board solder joints, comprising: providing an organic circuit board 11 (col. 1, lines 23-25) including a surface bearing electrical circuitry that includes at least a contact pad 16 (col. 4, lines 34-38); forming a solder mask layer 12 on surface, solder mask 12 being patterned to expose contact pad 16 (col. 4, lines 25-34); forming a thin metal seed layer 20 over surface, seed layer 20 being solely made of a first metal material (col. 5, lines 9-12); forming a resist layer 22 with at least one opening located at

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contact pad 16 that is deposited over seed layer 20 (col. 5, lines 17-26 and fig. 1); forming a solder bump 26 in opening by electroplating; solder bump containing at least first metal material (col. 5, lines 26-30 and 42-44 and fig. 1); and removing resist layer 22 and seed layer 20 beneath resist layer 22 (col. 6, lines 54-56 and fig. 3); wherein seed layer 20 beneath solder bump 28 dissolves completely into solder bump 28 after a reflow process, and disappears (col. 5, lines 44-49 and fig. 4).

Regarding claim 25, Higdon discloses first metal material 20 is copper (col. 5, lines 12-15).

Regarding claim 26, Higdon discloses seed layer 20 is 0.15-0.25 micrometer thick (less than 0.005 millimeter).

Regarding claim 27, Higdon discloses seed layer 20 is made of physical vapor deposition by sputtering method (col. 5, lines 9-12).

Regarding claim 32, Higdon discloses before forming thin metal seed layer 20, forming a barrier layer 18 on contact pad 16 (col. 4, lines 39-65).

Regarding claim 33, Higdon discloses barrier layer 18 is made of copper, nickel; chromium, titanium, copper-chromium alloy (col. 4, lines 39-65).

Regarding claim 34, Higdon discloses a method of forming electroplated solder on an organic circuit board for making flip chip joints and board to board solder joints, comprising: providing an organic circuit board 11 including a surface bearing electrical circuitry that includes at least a contact pad 16, contact pad 16 being made of copper (col. 4, lines 34-38);

forming a solder mask layer 12 on surface (col. 4, lines 25-34); solder mask 12 being patterned to expose contact pad 16; directly forming a thin metal seed layer 20 over surface, seed layer 20 being solely made of a first metal material which has good adhesion with copper (col. 5, lines 9-16); forming a resist layer 22 with at least one opening located at contact pad 16 that is deposited over seed layer 20 (col. 5, lines 17-19 and fig. 1); forming a solder bump 26 in opening by electroplating (col. 5, line 52 and fig. 3); and removing resist layer 22 and seed layer 20 beneath resist layer 22 (col. 6, lines 54-56 and fig. 3). Also note that Higdon does not teach requiring any roughness processing.

Regarding claim 35, Higdon discloses first metal material 20 is copper (col. 5, lines 12-15).

Regarding claim 36, Higdon discloses solder bump 28 containing at least first metal material 20, seed layer beneath solder bump 28 is disappeared due to dissolve completely into solder bump 28 after a reflow process (col. 5, lines 44-49).

Regarding claim 37, Higdon discloses seed layer 20 is made of physical vapor deposition by sputtering method (col. 5, lines 9-12).

Regarding claim 42, Higdon discloses before forming thin metal seed layer 20, forming a barrier layer 18 on contact pad 16, barrier layer 18 is made of metals which have good adhesion with both copper and the seed layer (col. 4, lines 39-65).

Regarding claim 43, Higdon discloses barrier layer 18 is made of metals selected from a group consisting of copper, nickel; chromium, titanium, copper-chromium alloy (col. 4, lines 39-65).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 28, 29, 38 and 39 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Higdon et al. (US Pat. 6,375,062) in view of Inaba et al. (US Pat. 6,387,734, herein after Ibana).

Higdon discloses a method of forming electroplated solder on an organic circuit board as describe above. However, Higdon fails to disclose seed layer is made of chemical vapor deposition or electroless plating method.

Inaba teaches seed layer is formed by chemical vapor deposition or electroless plating method (col. 5, lines 40-49).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Higdon by depositing a seed layer as taught by Inaba, in order to provide the well-known advantageous benefit of a smooth surface.

5. Claims 30, 31, 40 and 41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Higdon et al. (US Pat. 6,375,062) and Inaba et al. (US Pat. 6,387,734) as applied to claims 24, 29, 34 and 39 above, and further in view of Donovan et al. (US Pat. 3,958,048, herein after Donovan).

The combination of Higdon and Inaba discloses a method of forming electroplated solder on an organic circuit board as describe above. However, Higdon and Inaba fail to disclose performing a reduction process of copper ions to form a thin copper film on surfaces, wherein there is no reduction of noble metal ions such as palladium or platinum.

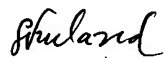
Donovan teaches a process for electroless plating a copper layer by coating the surfaces with aqueous solutions which at least contains copper ions and then performing a reduction process of copper ions to form a thin copper film on surfaces, wherein there is no reduction of noble metal ions such as palladium or platinum (col. 8, lines 50-59).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Higdon and Inaba by depositing a seed layer as taught by Donovan, because the manufacture of the model chip scale package would have been relatively simplified and economical (col. 3, lines 54-60).

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (571) 272-1798. The examiner can normally be reached on Monday-Friday from 8:00am to 5:00pm. If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



David Vu

May 13, 2004